

**Listing of Claims**

1. (Currently Amended) A system, comprising:  
a first interpolator to adjust a phase of an in-phase signal; and  
a second interpolator to adjust a phase of a quadrature signal,  
wherein the second interpolator is to adjust ~~adjusts~~ the quadrature signal phase independently from the phase adjustment of the in-phase signal performed by the first interpolator, the first interpolator to adjust the phase of the in-phase signal to coincide with a first predetermined point on an eye diagram, and the second interpolator to adjust the phase of the quadrature signal to coincide with a second predetermined point on an eye diagram,  
wherein at least one of the phase of the in-phase signal or the phase of the quadrature signal is to be adjusted so that the phase of the in-phase signal at the first predetermined point is non-orthogonal to the phase of the quadrature signal at the second predetermined point on the eye diagram.
2. (Canceled)
3. (Original) The system of claim 1, wherein the second interpolator adjusts the quadrature signal phase based on one or more predetermined increments.
- 4-6 (Canceled)
7. (Previously Presented) The system of claim 1, wherein the second predetermined point is a widest point on the eye diagram.

8. (Original) The system of claim 1, further comprising:

a controller which sets at least one configuration value of the second phase interpolator, wherein the second phase interpolator adjusts the quadrature signal phase independently from the phase of the in-phase signal based on said at least one configuration value.

9. (Original) The system of claim 8, wherein said at least one configuration value includes an offset value for the quadrature signal phase.

10. (Currently Amended) A system, comprising:

a demodulator to generate in-phase and quadrature signals from a data signal; and  
a phase adjuster to adjust a phase of the quadrature signal independently from a phase of the in-phase clock signal, wherein the adjusted phase of the quadrature signal corresponds to a clock signal, wherein a non-orthogonal relationship exists between the phases of the quadrature and in-phase signals after said adjustment, the adjusted phases of the in-phase and quadrature signals corresponding to non-orthogonal positions on an eye diagram.

11. (Canceled)

12. (Original) The system of claim 10, wherein the phase adjuster adjusts the quadrature signal phase based on one or more predetermined increments.

13. (Original) The system of claim 10, further comprising:

a sampler which samples the data signal based on said clock signal.

14. (Currently Amended) A method, comprising:  
generating in-phase and quadrature signals from a data signal; and  
adjusting a phase of the quadrature signal independently from a phase of the in-phase signal, wherein said adjusting includes:

adjusting the phase of the in-phase signal to coincide with a first point on the eye diagram and adjusting the phase of the quadrature signal to coincide with a second point on the eye diagram, wherein at least one of the phase of the in-phase signal or the phase of the quadrature signal is adjusted so that the phase of the in-phase signal at the first point is non-orthogonal to the phase of the quadrature signal at the second point on the eye diagram  
~~results in a non-orthogonal relationship between the phases of the quadrature and in-phase signals.~~

15-16 (Canceled)

17. (Currently Amended) The method of claim 14 ~~46~~, wherein the second ~~first~~ point is a widest point on the eye diagram.

18. (Canceled)

19. (Currently Amended) The method of claim 14 ~~48~~, wherein the second ~~first~~ point is a widest point and the first ~~second~~ point is a crossing point in the eye diagram.

20. (Original) The method of claim 14, wherein adjusting the quadrature signal phase includes:

mapping a phase of the quadrature signal onto an eye diagram of the data signal;

determining a difference between the phase of the quadrature signal and a phase which coincides with a first point on the eye diagram; and

adjusting the quadrature signal phase to reduce said difference.

21. (Original) The method of claim 20, wherein the quadrature signal phase is adjusted to at least substantially eliminate said difference.

22. (Original) The method of claim 20, wherein the quadrature signal phase is adjusted in one or more predetermined increments to reduce said difference.

23. (Original) The method of claim 20, wherein the first point is a widest point on the eye diagram.

24. (Original) The method of claim 20, wherein adjusting the quadrature signal phase to reduce said difference does not change the phase of the in-phase signal.

25. (Original) The method of claim 14, further comprising:

sampling the data signal based on the adjusted quadrature signal phase.

26. (Currently Amended) A system, comprising:
- a first circuit; and
  - a second circuit which includes:
    - (a) a demodulator to generate in-phase and quadrature signals from a data signal; and
    - (b) a phase adjuster to adjust a phase of the quadrature signal independently from a phase of the in-phase clock signal, wherein the adjusted phase of the quadrature signal corresponds to a clock signal used to control the first circuit, wherein a non-orthogonal relationship exists between the phases of the quadrature and in-phase signals after said adjustment, the adjusted phases of the in-phase and quadrature signals corresponding to non-orthogonal positions on an eye diagram.
27. (Canceled)
28. (Original) The system of claim 26, wherein the phase adjuster adjusts the quadrature signal phase based on one or more predetermined increments.
29. (Original) The system of claim 26, wherein the first circuit is at least one of a processor and a memory.
30. (Original) The system of claim 26, wherein the first circuit and second circuit are included on a same chip die.

31. (Currently Amended) The system of claim 1 [[6]], wherein the second interpolator adjusts the phase of the quadrature signal to the second predetermined point by one or more predetermined phase increments.

32. (Previously Presented) The system of claim 31, wherein a difference between the phase of the in-phase signal at the first predetermined point and the phase of the quadrature signal at the second predetermined point corresponds to said one or more predetermined phase increments.

33. (Previously Presented) The system of claim 31, further comprising:  
sampling the in-phase and quadrature phase signals based on the independently adjusted phases of the in-phase and quadrature signals.